



843.37558VX2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

Applicant(s): FUNABASHI

Serial No.: 09/902,672

Filed: July 12, 2001

For: METHOD OF MANUFACTURING A SEMICONDUCTOR  
INTEGRATED CIRCUIT DEVICE

Group: 2825

Examiner: L. Malsawma

TECHNOLOGY CENTER 2800

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**NOTICE OF APPEAL FROM THE PRIMARY  
EXAMINER TO THE BOARD OF APPEALS**

Assistant Commissioner for Patents  
Washington, D.C. 20231

January 16, 2003

Sir:

In accordance with the provisions of 37 CFR 1.191, applicant hereby appeals from the final rejection as set forth in the Official Action mailed July 18, 2002, finally rejecting claims 17-30.

A Credit Card Payment form in the amount of \$320.00 is enclosed, to cover the Appeal Fee.

01/17/2003 MGBREM1 00000018 09902672

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320.00 OP

Respectfully submitted,

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